

**RECEIVER OF DIGITAL SIGNALS HAVING A VARIABLE HYSTERESIS, IN  
PARTICULAR FOR AUDIO DIGITAL APPLICATION**

**ABSTRACT**

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The invention relates to a signal receiver inserted between a first and a second voltage reference and having a first and a second input terminal effective to receive differential signals and an output terminal effective to provide a converted signal.

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Advantageously, the signal receiver according to the invention comprises a conversion stage inserted between the first and second voltage references and connected between the first and second input terminals of the signal receiver and an input terminal of an hysteresis comparator, connected in turn to the output terminal of the signal receiver. In particular, the conversion stage performs a conversion from any input signal received on respective input terminals to an intermediate signal provided on an output terminal and suitable for reception by the hysteresis comparator.

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